

# PATENT ABSTRACTS OF JAPAN

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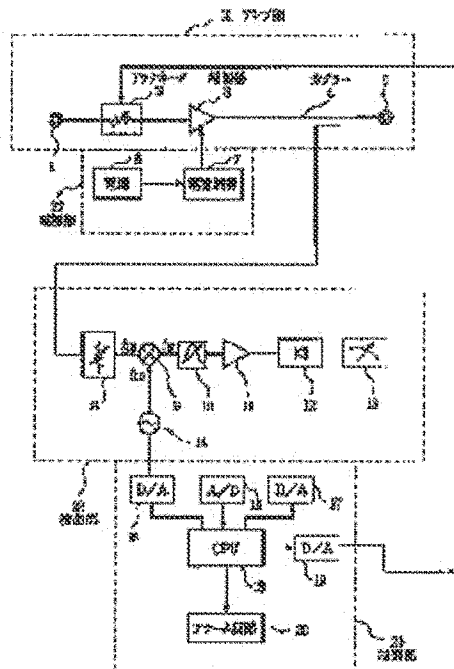
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## (54) POWER CONSUMPTION REDUCING SYSTEM FOR HIGH FREQUENCY AMPLIFIER CIRCUIT



### (57)Abstract:

PURPOSE: To perform the current control of an amplifier so as to set a current that flows on the amplifier at the minum level in the allowable range of generating quantity of intermodulation distortion by detecting the level of intermodulation distortion generated in the amplifier in a high frequency amplifier circuit.

CONSTITUTION: An RF signal including the intermodulation distortion generated in the amplifier 3 is fetched from a coupler 4, and it is detected as sweeping a frequency band where the intermodulation distortion is generated by a detecting part 23, and the level of the signal is read sequentially by a CPU 18. The CPU 18 controls a current control circuit 7 so as to set current consumption at the minimum level within the allowable range of the level of the intermodulation distortion, and controls an attenuator 2 so as to keep the level of an RF carrier constant

simultaneously. Also, when the intermodulation distortion exceeds the allowable range, an alarm is issued by operating an alarm circuit 20.